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	Filing Date		2007-06-13		
	First Named Inventor	Johan	nnus Theodorus Matheus Hubertus Dielissen		
	Art Unit		2416 2471		
	Examiner Name	Kouro	oush Mohebbi		
	Attorney Docket Numb	er	NL040218US1		

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	1	E. RIJPKEMA ET AL, "TRADE OFFS IN THE DESIGN OF A ROUTER WITH BOTH GUARANTEED AND BEST- EFFORT SERVICES FOR NETWORKS ON CHIP", DESIGN, AUTOMATION AND TEST IN EUROPE CONFERENCE NAD EXHIBITION, MARCH 3-7, 2003, MUNICH, GERMAN						
	2	T.T. YE ET AL, "PACKETIZED ON-CHIP INTERCONNECT COMMUNICATION ANALYSIS FOR MPSoC", DESIGN AUTOMATION AND TEST IN EUROPE, PROCEEDINGS, 2003, PAGES 344-349						
	3	J. DIELISSEN ET AL, "CONCEPTS AND IMPLEMETATION OF THE PHILIPS NETWORK-ON-CHIP", INTERNET ARTICLE, NOVEMBER 2003, XP002330547, PAGES 1-6						
	4	E. RIJPKEMA ET AL, "A ROUTER ARCHITECTURE FOR NETWORKS ON SILICON", PROCEEDINGS OF PROCESS 2001, 2ND WORKSHOP ON EMBEDDED SYSTEMS						
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